

Figure 1 shows the layout of the floating-point registers in the floating-point unit. The floating-point unit contains 16 floating-point registers, each 64 bits wide. The registers are organized into two groups of 8 registers each. The first group of 8 registers is the floating-point control register, and the second group of 8 registers is the floating-point status register. The floating-point control register contains the floating-point control fields, and the floating-point status register contains the floating-point status fields. The floating-point registers are used to store floating-point data, and the floating-point control and status registers are used to control and monitor the floating-point unit.

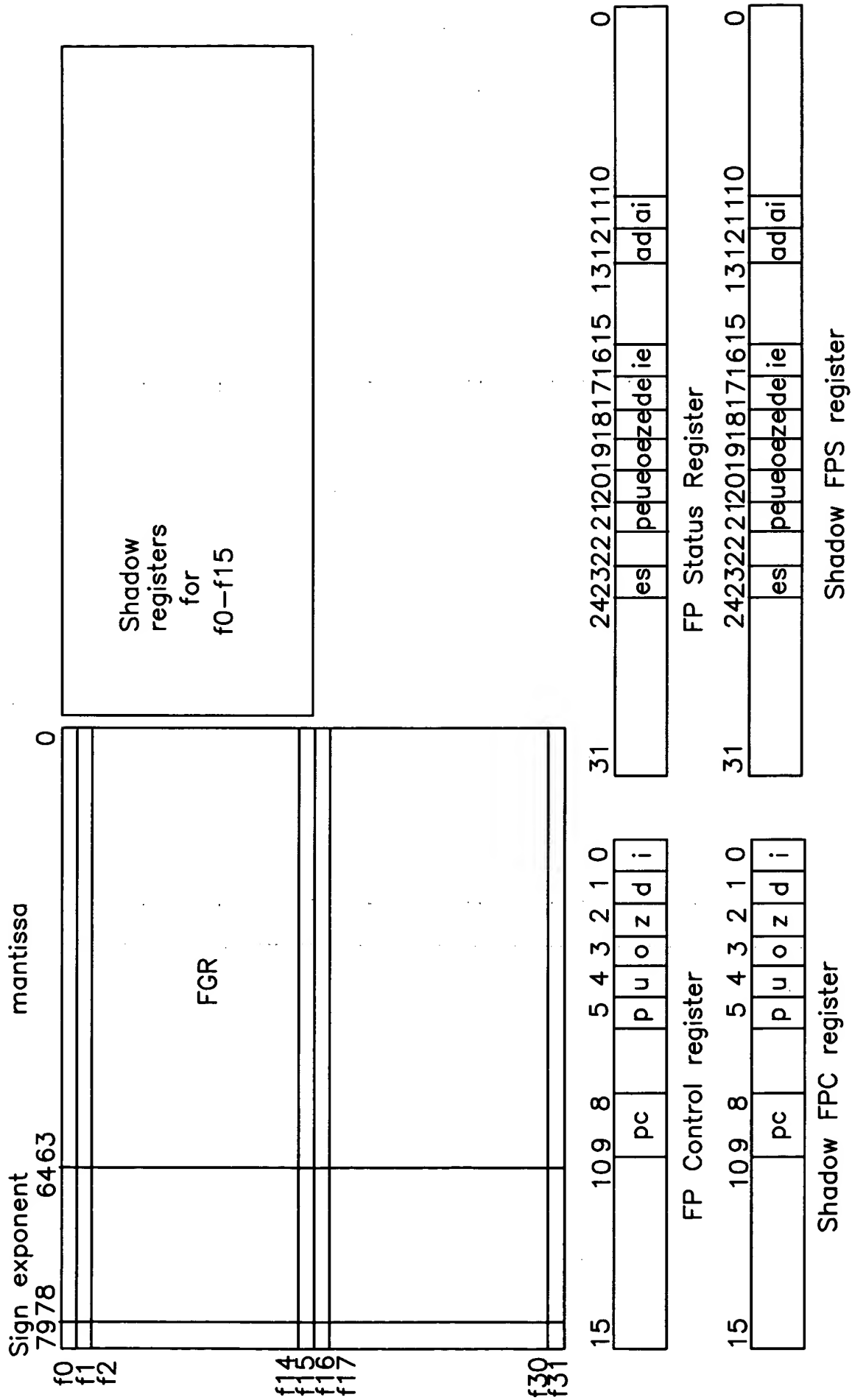


Figure 1

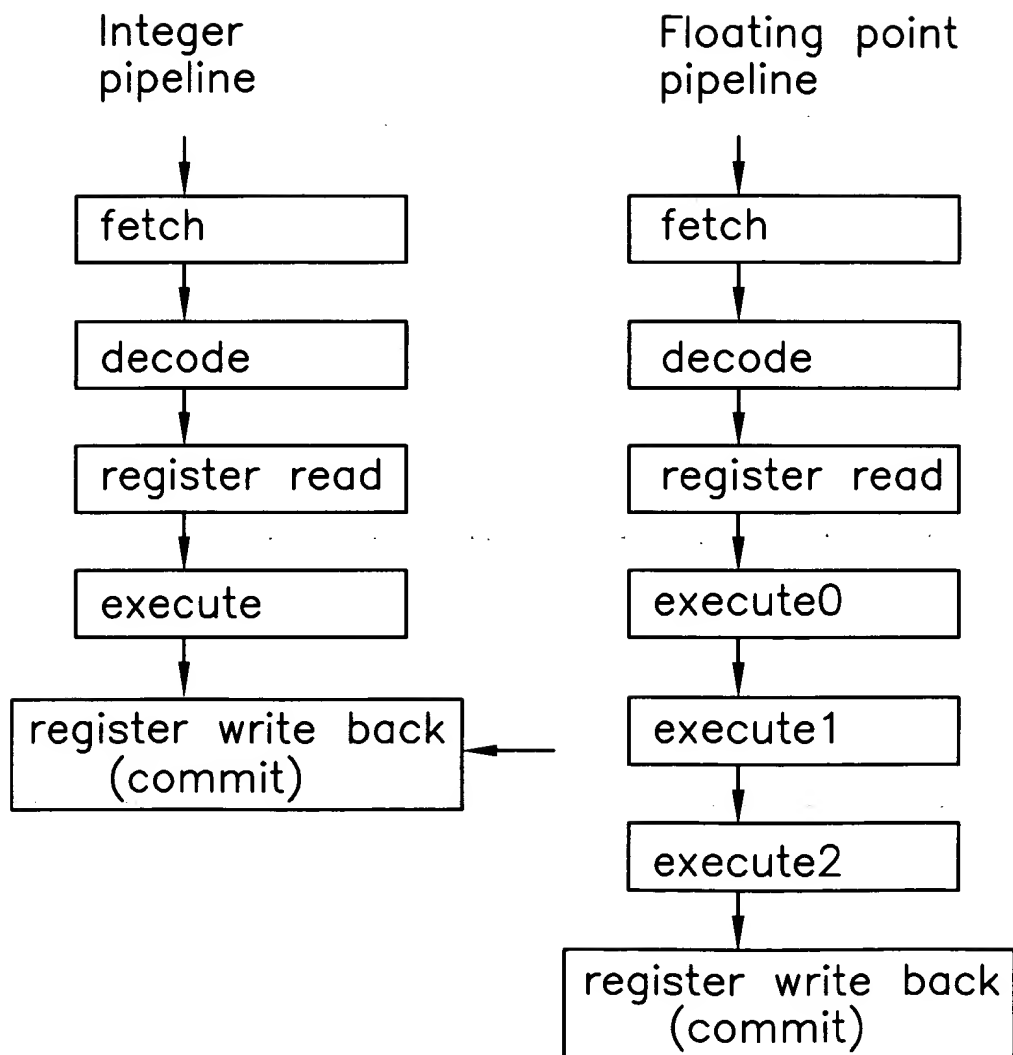


Figure 2